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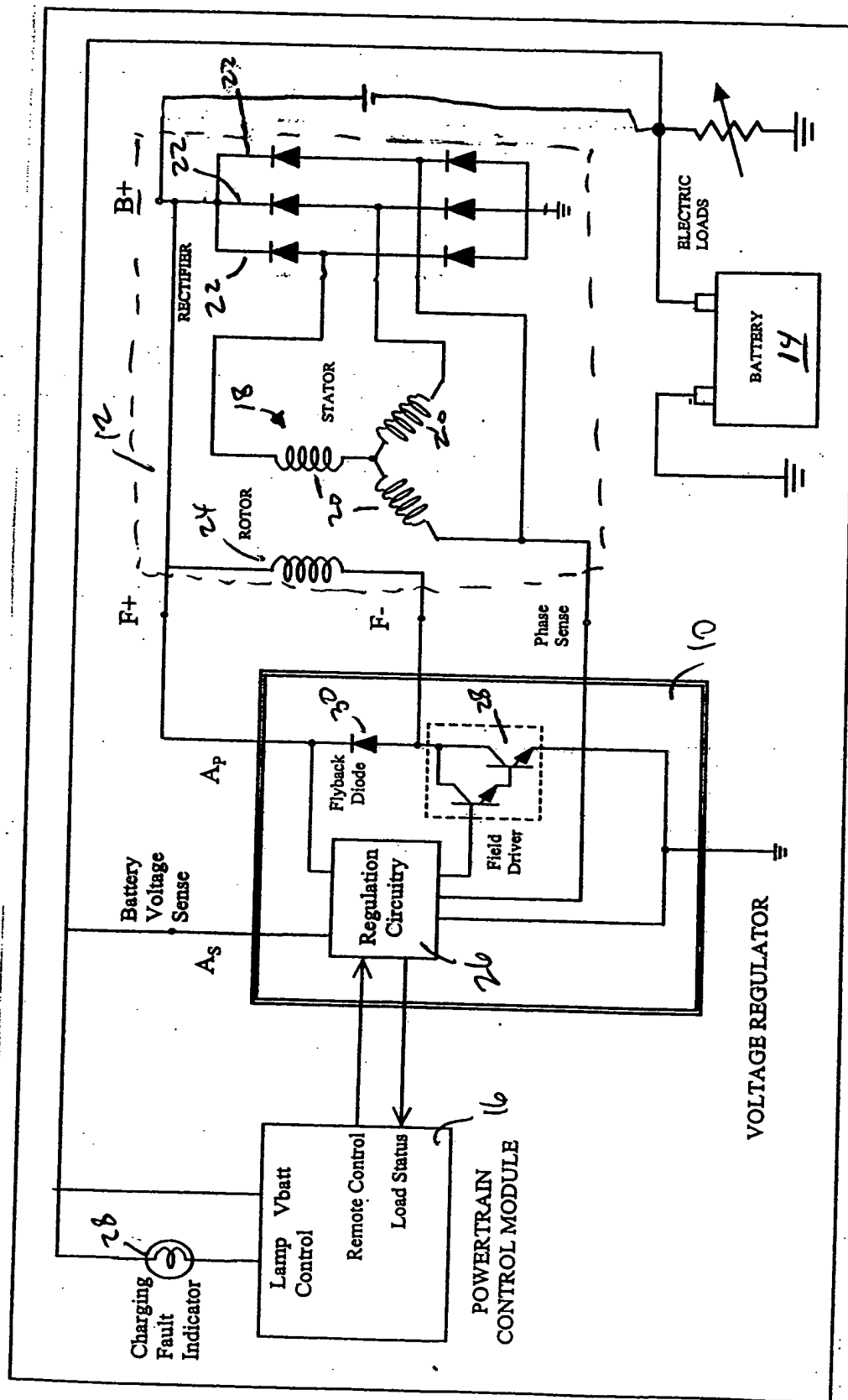


Figure 1  
(prior Art)

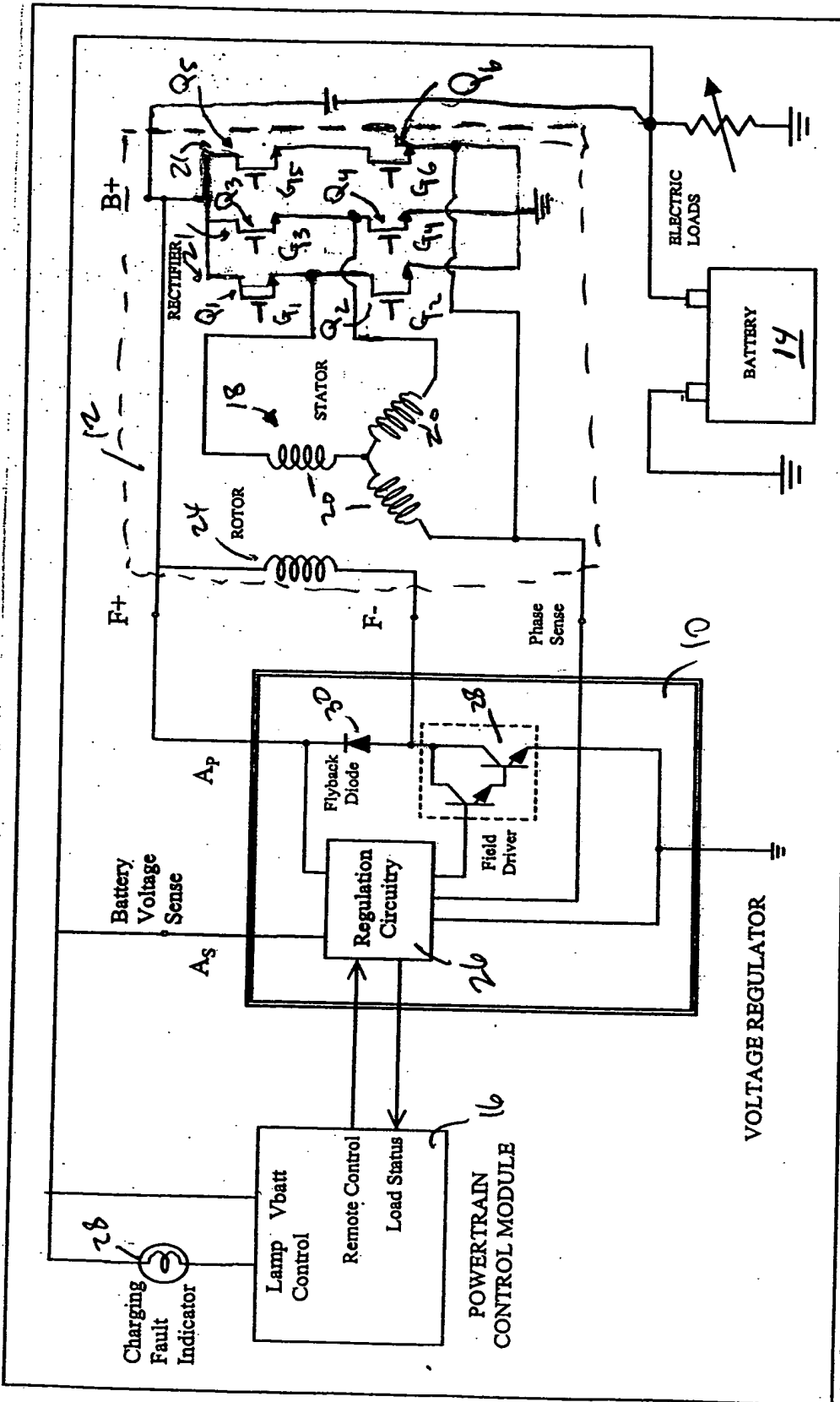


Figure 2

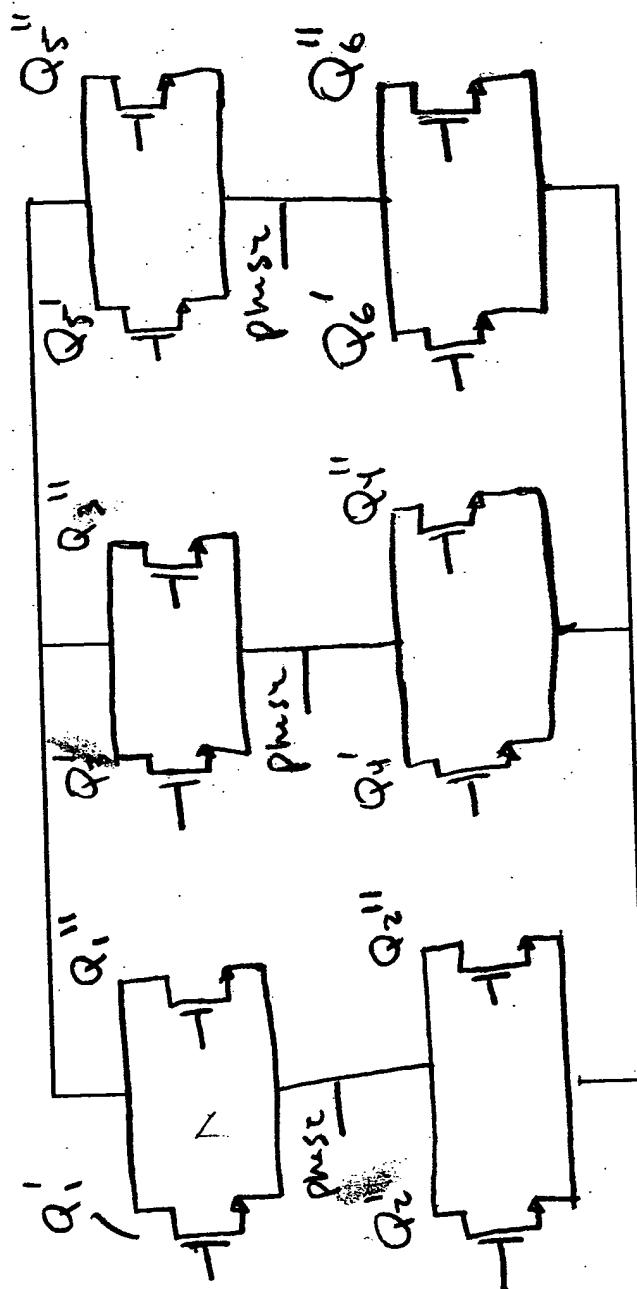


Figure 3

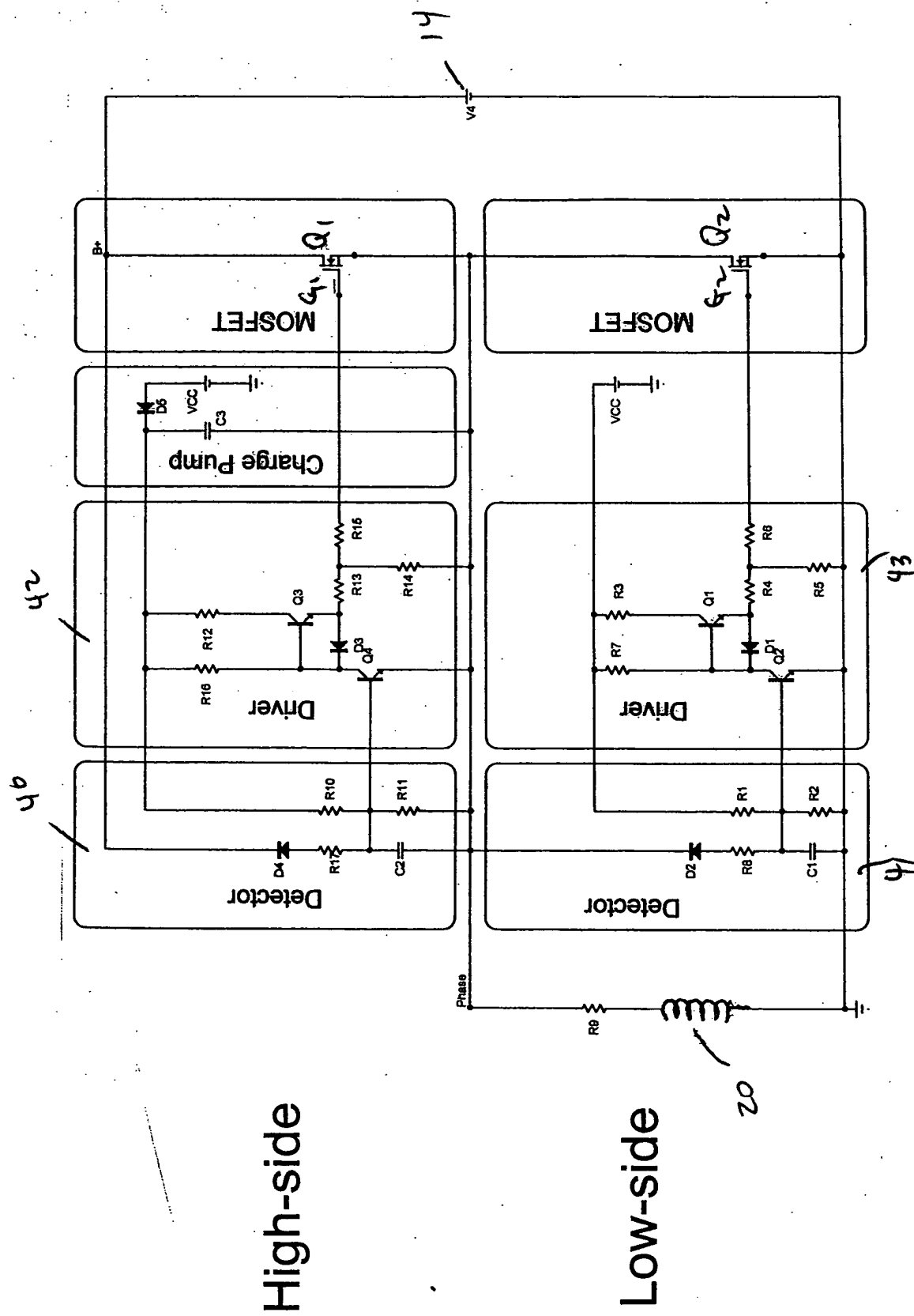


Figure 4

# Gate voltage, Phase voltage, low current

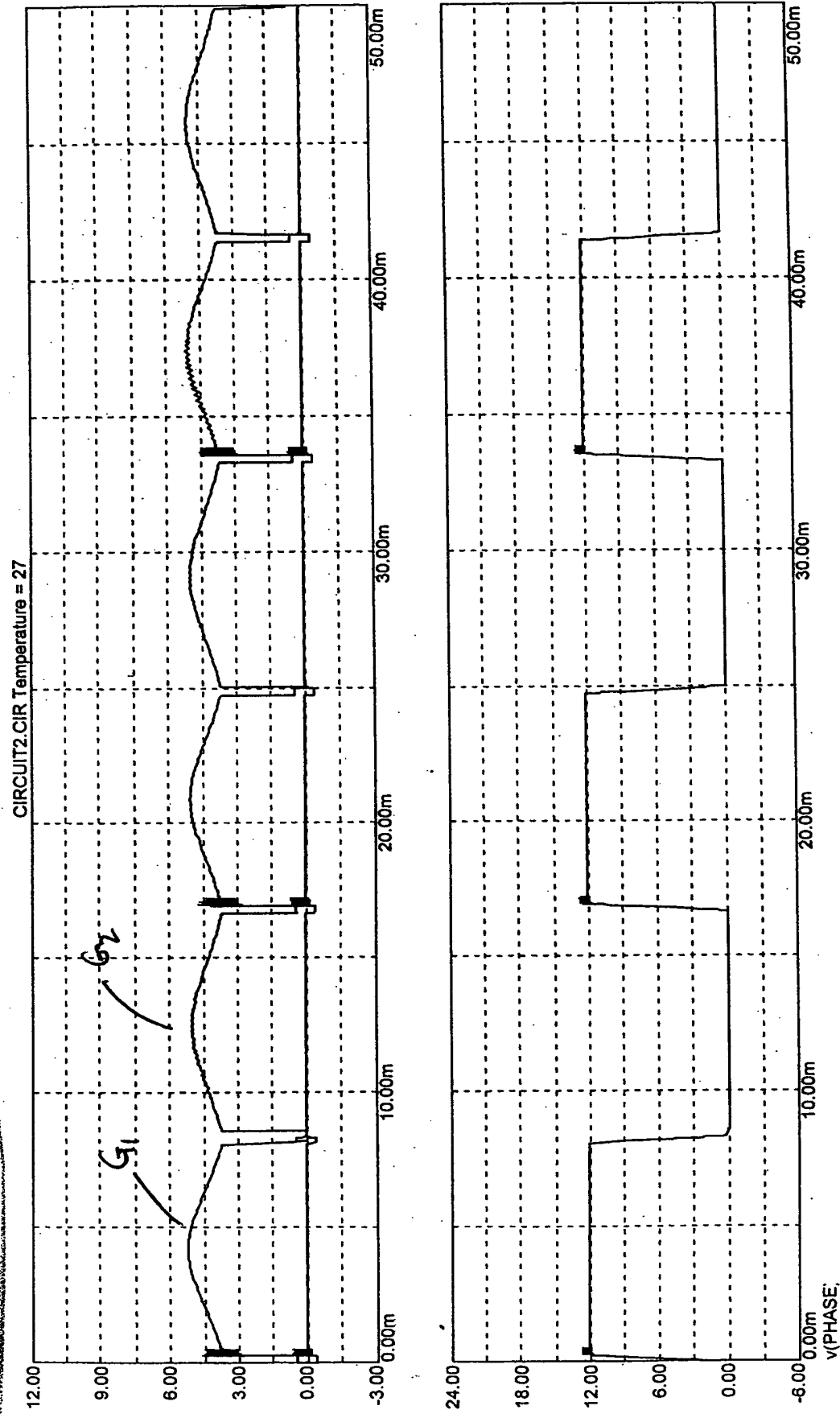


Figure 5-A

# Gate voltage, Phase voltage, medium current

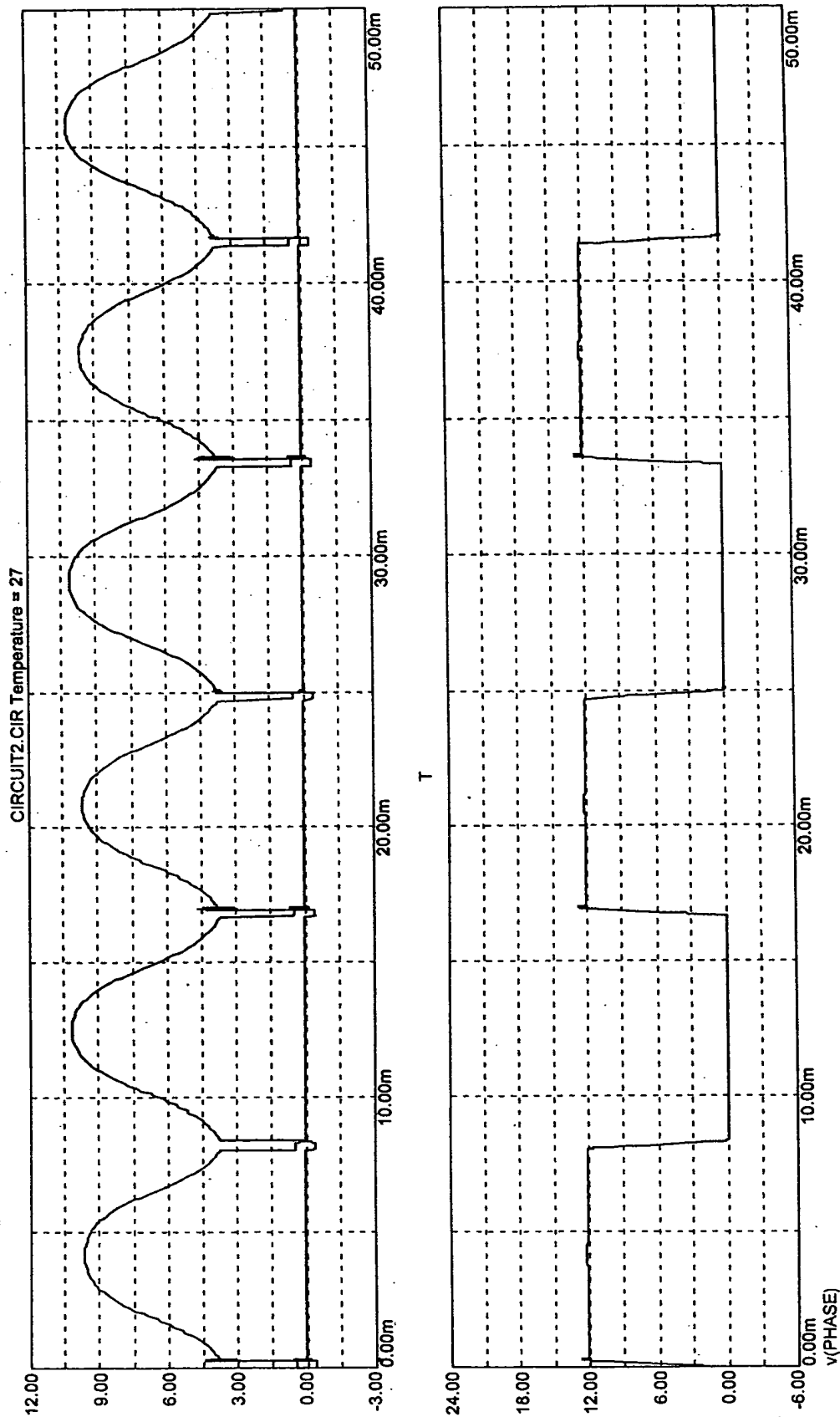


Figure 5B

# Gate voltage, Phase voltage

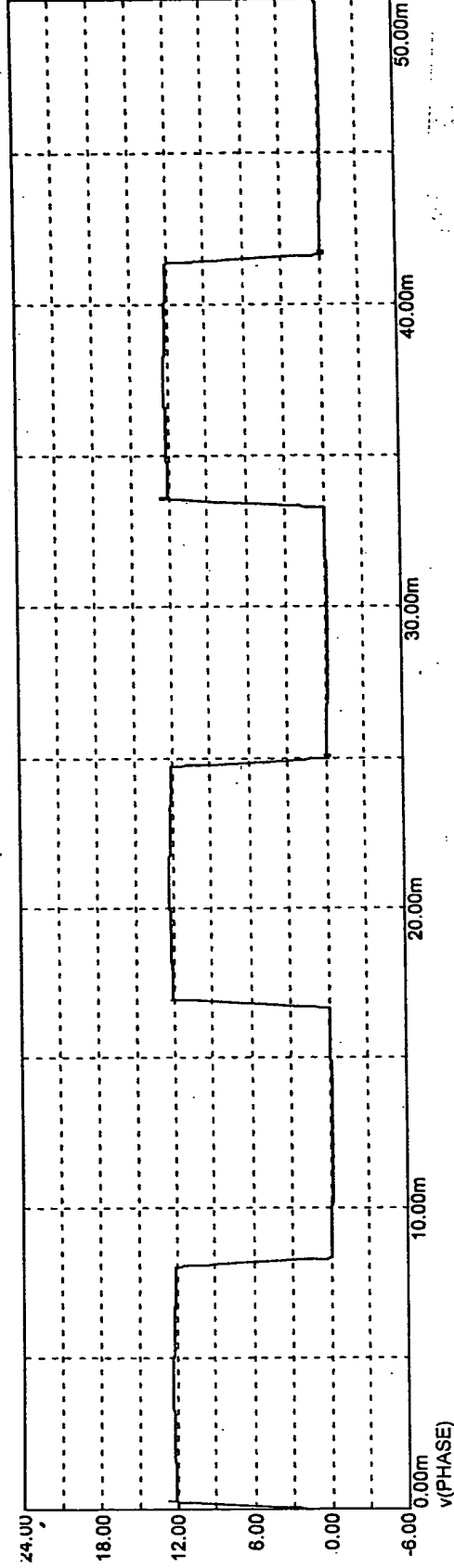
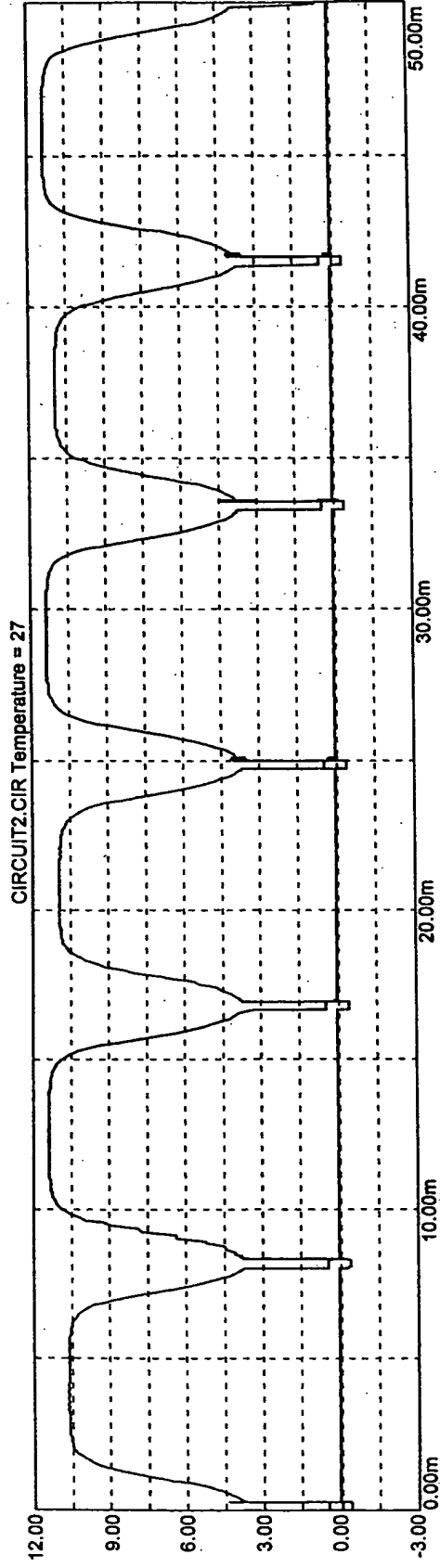
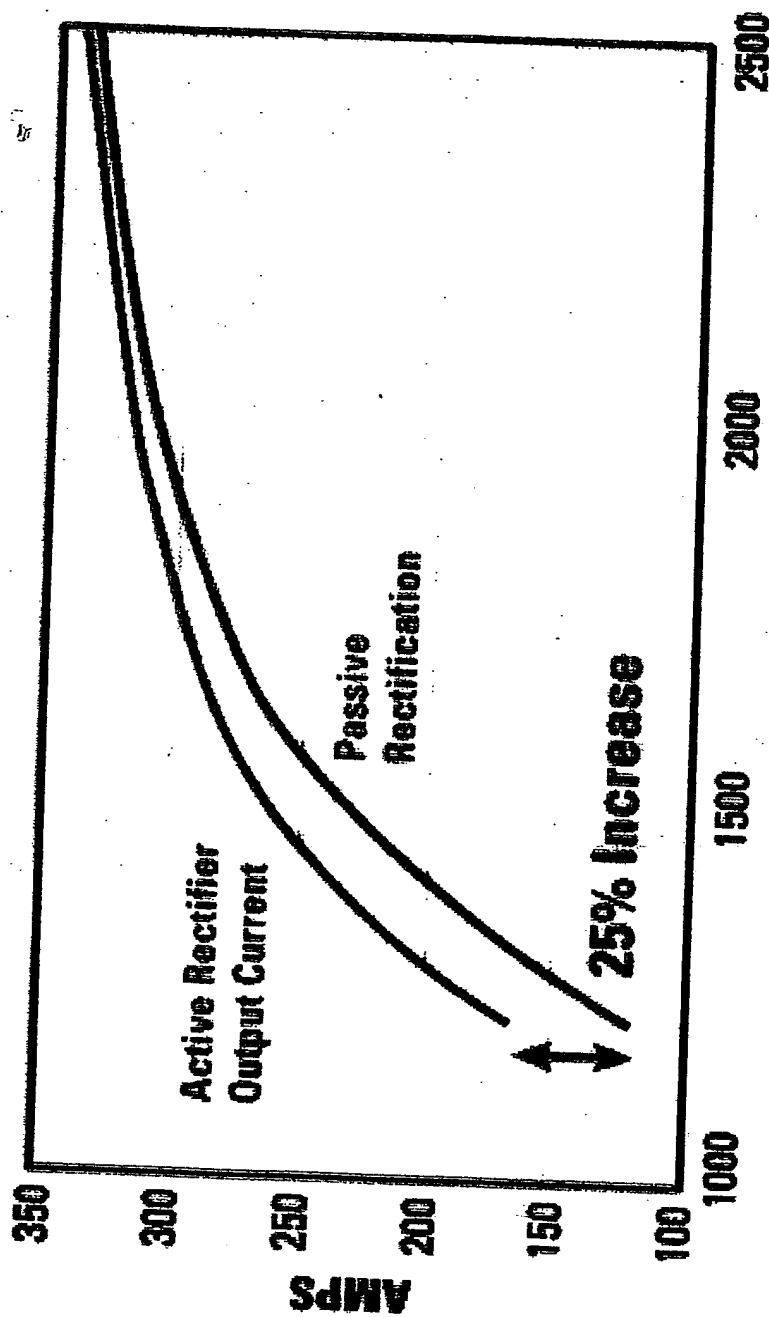


Figure 5C



RPM

Figure 6

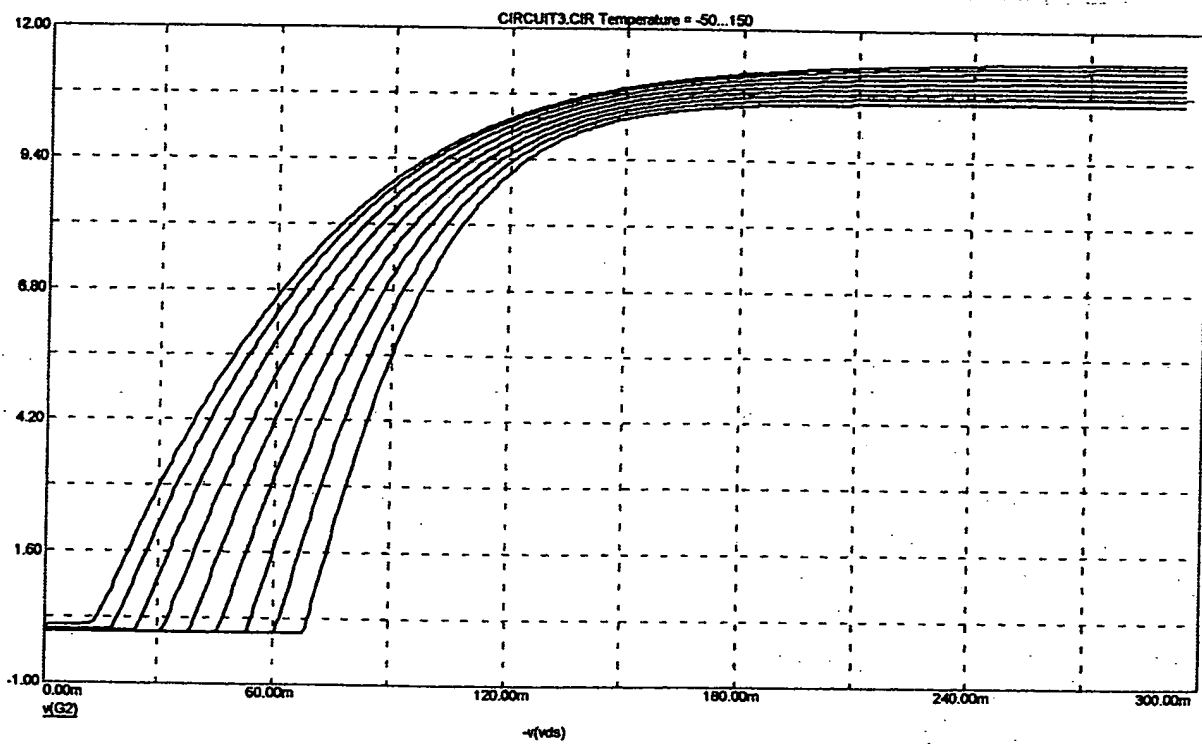


Figure 7

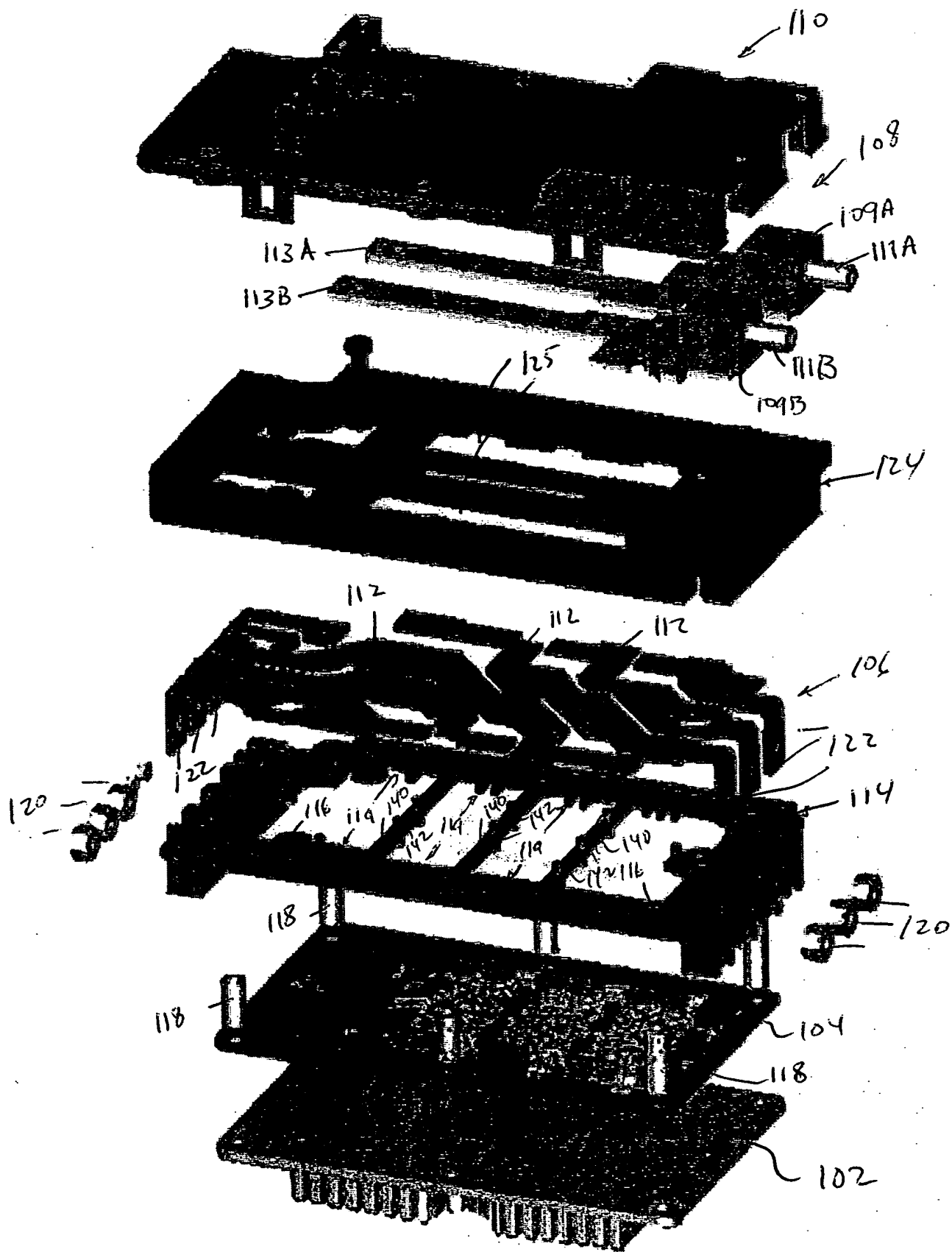


Figure 8

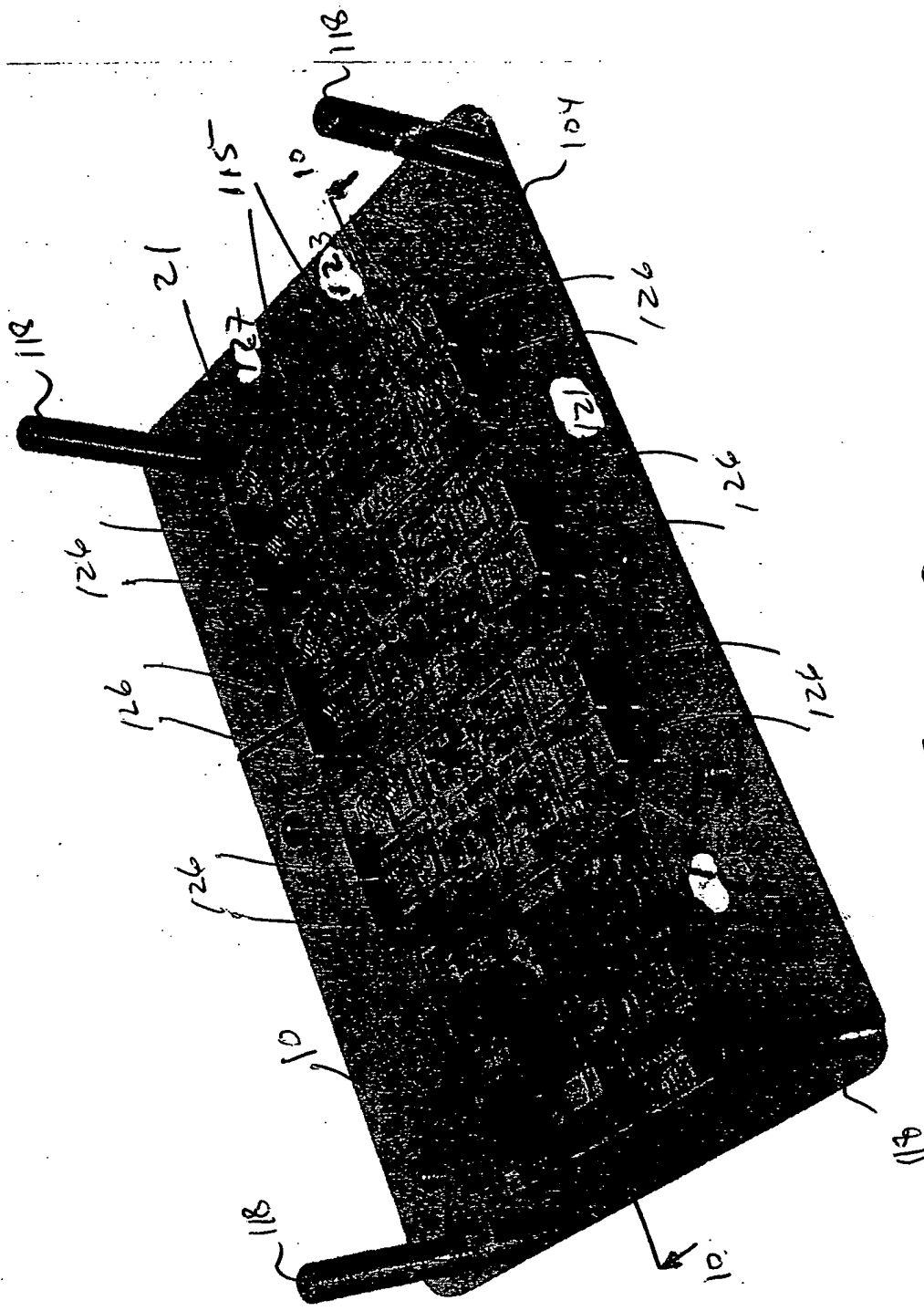


Figure 9

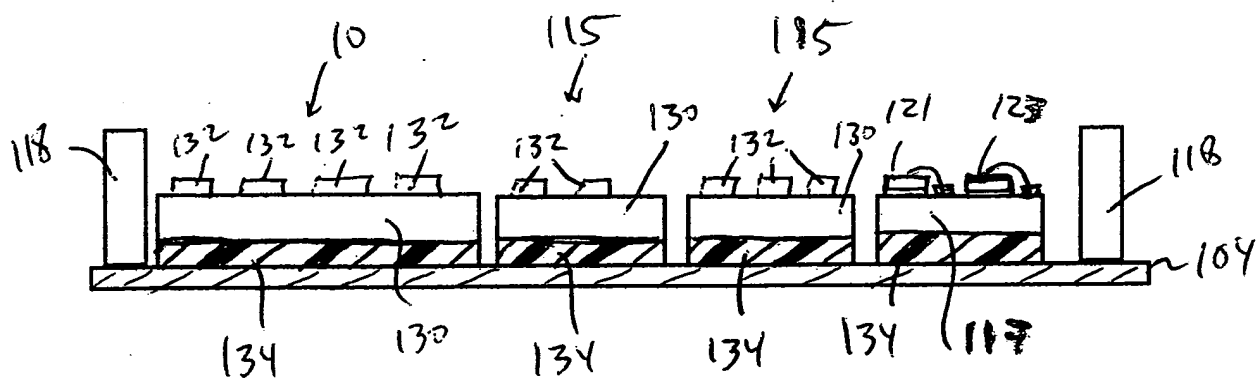


Figure 10

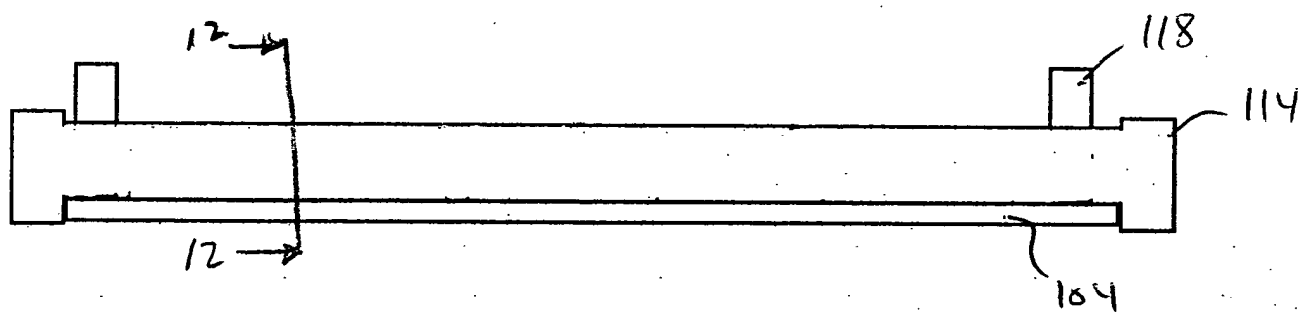


Figure 11

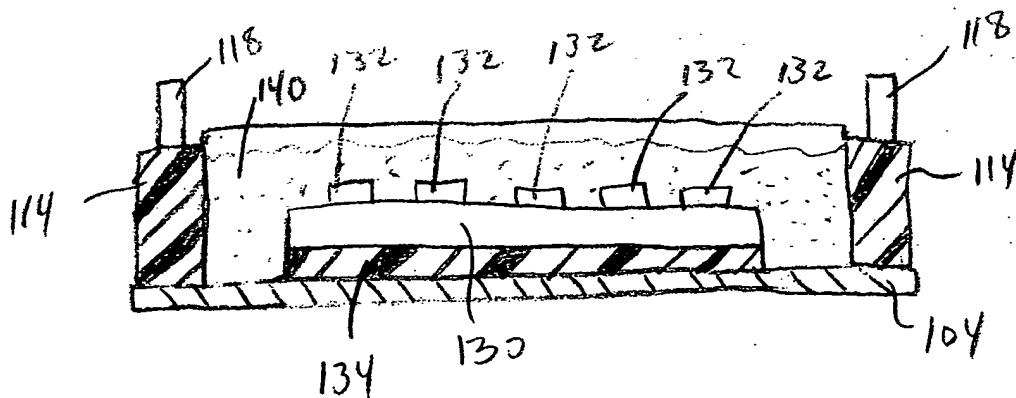


Figure 12

Figure 13A

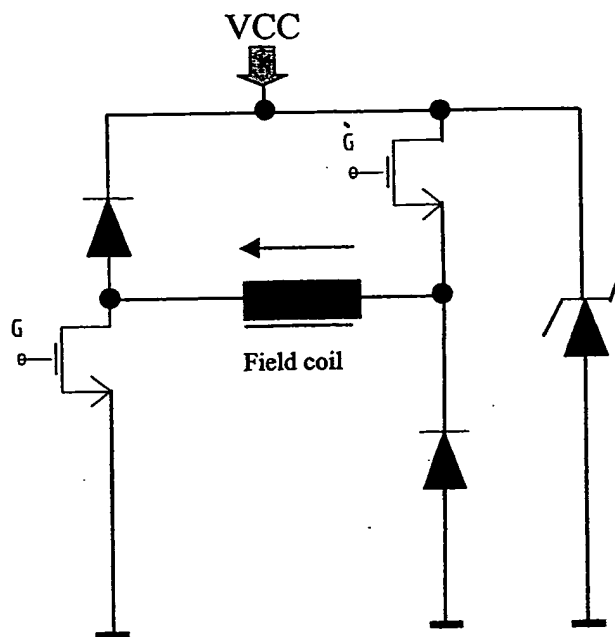


Figure 13B

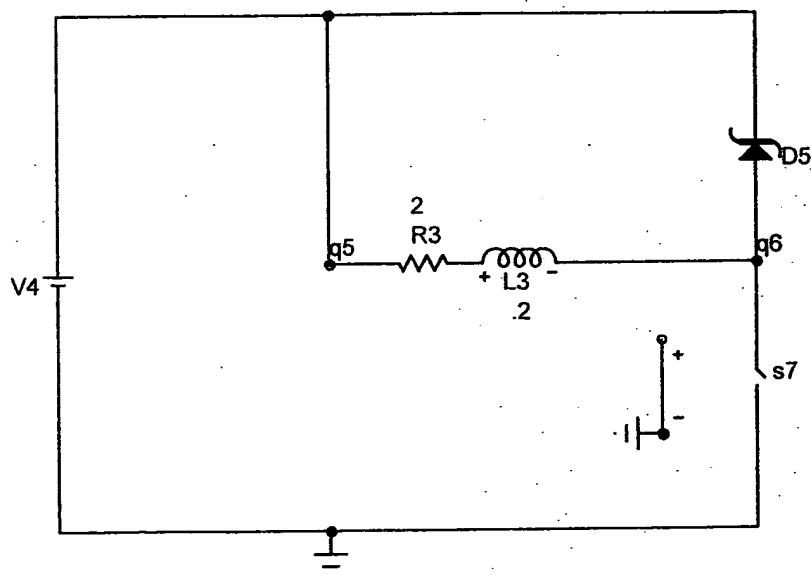


Figure 13C

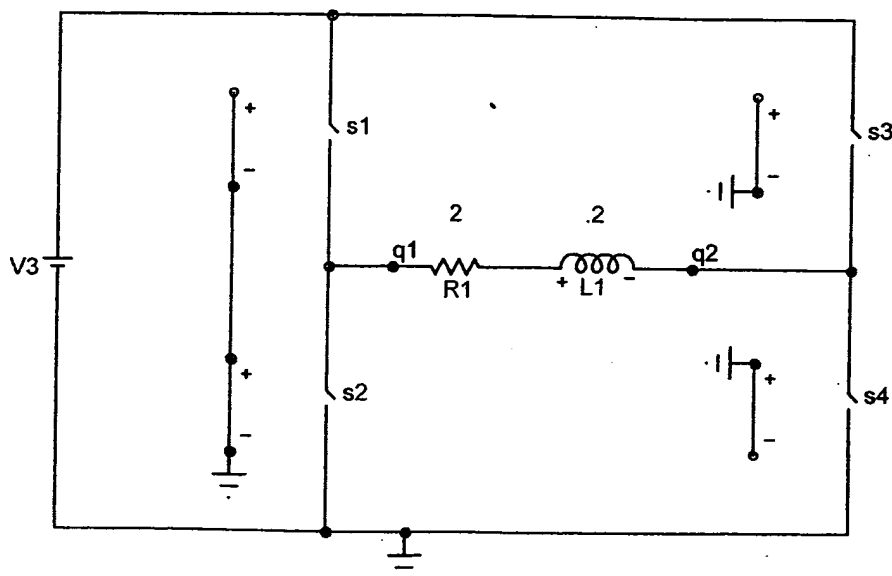


Figure 13D

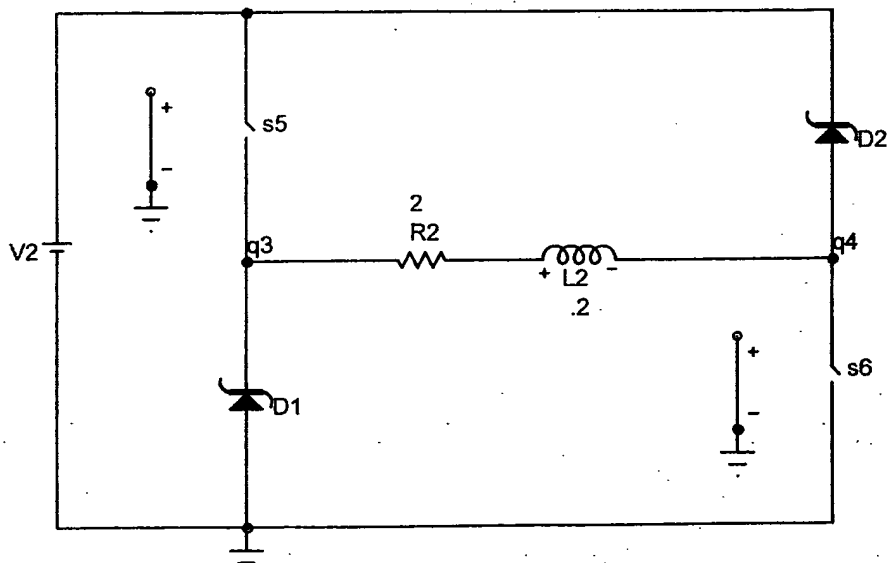
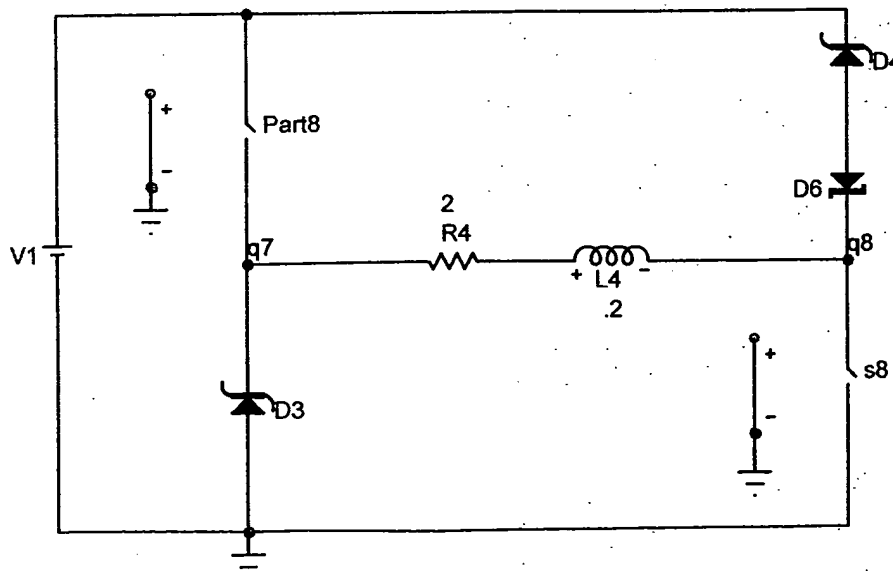
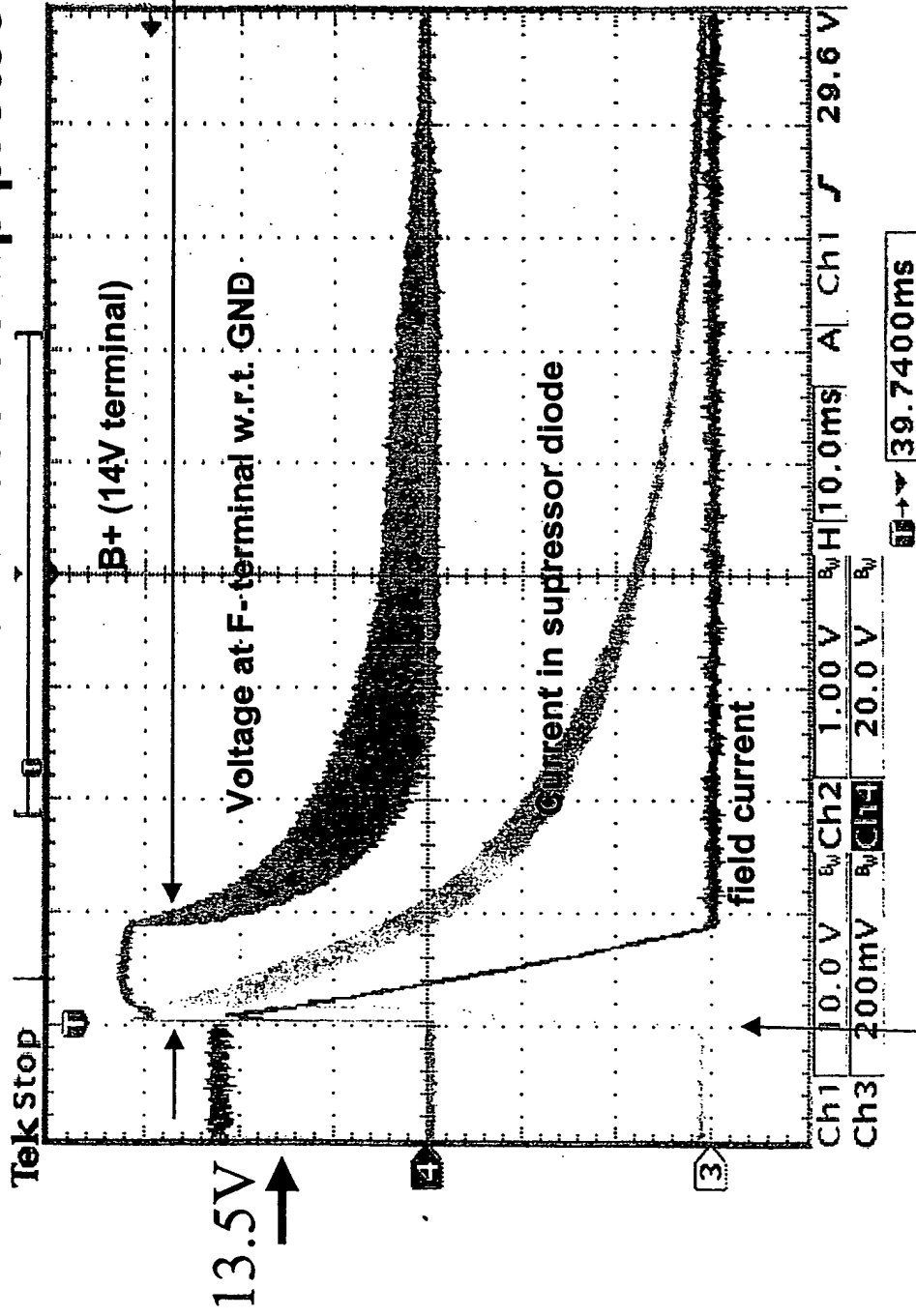


Figure 13E



# Active load dump protection



8 Feb 2001  
14:17:15

Figure 14